

Electrical Performance

Efficiency(%)	25.30	25.20	25.10	25.00	24.90	24.80	24.70	24.60	24.50
Voc(V)	0.727	0.726	0.725	0.724	0.723	0.722	0.721	0.720	0.719
Isc(A)	13.895	13.864	13.837	13.819	13.785	13.750	13.715	13.680	13.645
Vmpp(V)	0.625	0.624	0.623	0.622	0.621	0.620	0.619	0.618	0.617
Imp(A)	13.552	13.517	13.485	13.453	13.421	13.388	13.356	13.323	13.291
Pmpp(W)	8.47	8.43	8.40	8.37	8.33	8.30	8.27	8.23	8.20

Standard Test Conditions: 1000W/m², AM1.5, 25°C

Appearance and Structure

Substrate material	N-type mono-crystalline silicon wafer-TOPCon
Cell thickness	130μm±13μm
Dimension	182.2mm×183.75mm±0.25mm
Diagonal	256mm±0.25mm
Front(-)	16 bus bars, 172 lines, Silicon oxide + blue silicon nitride compound anti reflection coating
Back(+)	16 bus bars, 200 lines, Silicon oxide + blue silicon nitride compound anti reflection coating

Temperature Coefficient

TkPower	-(0.33±0.02) %/k
TkVoltage	-(0.27±0.03) %/k
TkCurrent	+(0.045±0.015) %/k

Light induced degradation

Using Xenon lamp (Irradiance of 1000W/ m², with spectrum AM 1.5) to irradiate test cells, after a total irradiation of 5 kwh/ m², the degradation of maximum output power of cells is ≤2%.

Anti-PID

Potential Induced Degradation(-1500V, 192h):≤5%.

Packaging, Storage

Solar cells are closely packed with soft sponge around and heat shrink is used around the box unit. Outer packing box must have shock buffer, to be suitable for long-distance delivery.

After packaging, cells should be stored indoors in the conditions of humidity below 60%, and temperature (20±10) °C. Cells should be sampling inspected again if the storage time over 180 days.